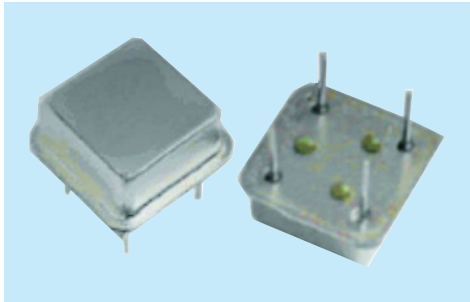




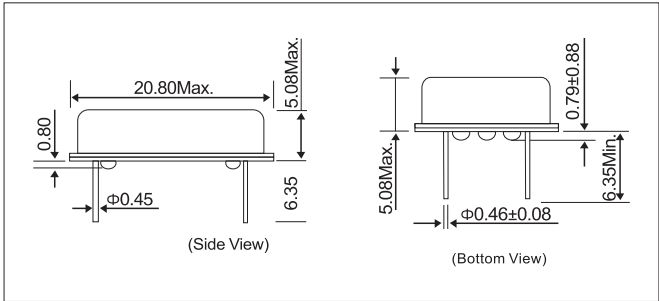
Products Information

VC08



- DESCRIPTION:
- Stability up to $\pm 10 \times 10^{-6}$
 - Control Voltage Range $\pm 200 \times 10^{-6}$ Max.
 - TTL/HCMOS Outout
 - 8PIN Package
 - SDH/SONET
 - ATM
 - WLL
 - Measurement Equipment
 - PLL Circuits

RECOMMENDED LAND PATTERN [mm]



DIMENSION [mm]

PIN	FUNCTION
#1	VC
#4	GND
#5	OUTPUT
#8	+DC

PERFORMANCE CHARACTERISTICS

Parameter		Condition	VC08	
Frequency Range		F ₀	1.544MHz~50.000MHz	
Frequency Stability		All Condition	See Next Table	
Operating Temperature Range		T _{OPR}	See Next Table	
Linearity			±10%	
Supply Voltage		V _{DD}	B: +5.0VDC ± 10%	A: +3.3VDC ± 10%
Supply Current		1.544M ≤ F ₀ < 10M	10mA Max.	7mA Max.
		10M ≤ F ₀ < 26M	20mA Max.	15mA Max.
		26M ≤ F ₀ < 50M	25mA Max.	20mA Max.
Output Load			A: TTL 15pF C: CMOS 15pF D: CMOS 50pF	
Output Duty		1.4V or 1/2V _{DD}	45%~55%	
Control Voltage Range			See Ordering Information	
Frequency Stability	Supply Voltage change	±5%	±3×10 ⁻⁶	
	Load change	±10%	±3×10 ⁻⁶	
	Aging		±3×10 ⁻⁶ /year	
Rise Time	T _r	0.4V~2.4V	10nS Max.	
		10%V _{DD} ~90%V _{DD}		
Fall Time	T _f	2.4V~0.4V	10nS Max.	
		90%V _{DD} ~10%V _{DD}		
Output Level	“0”Level	V _{OL}	0.4V Max. or 10%V _{DD}	
	“1”Level	V _{OH}	2.4V Min. or 90%V _{DD}	
Start-Up Time		T _s	10mS Max.	
Vc Input Impedance			100KΩ	
Storage Temperature Range		T _{STG}	-55°C~+125°C	
Package			DIP8	

JDS

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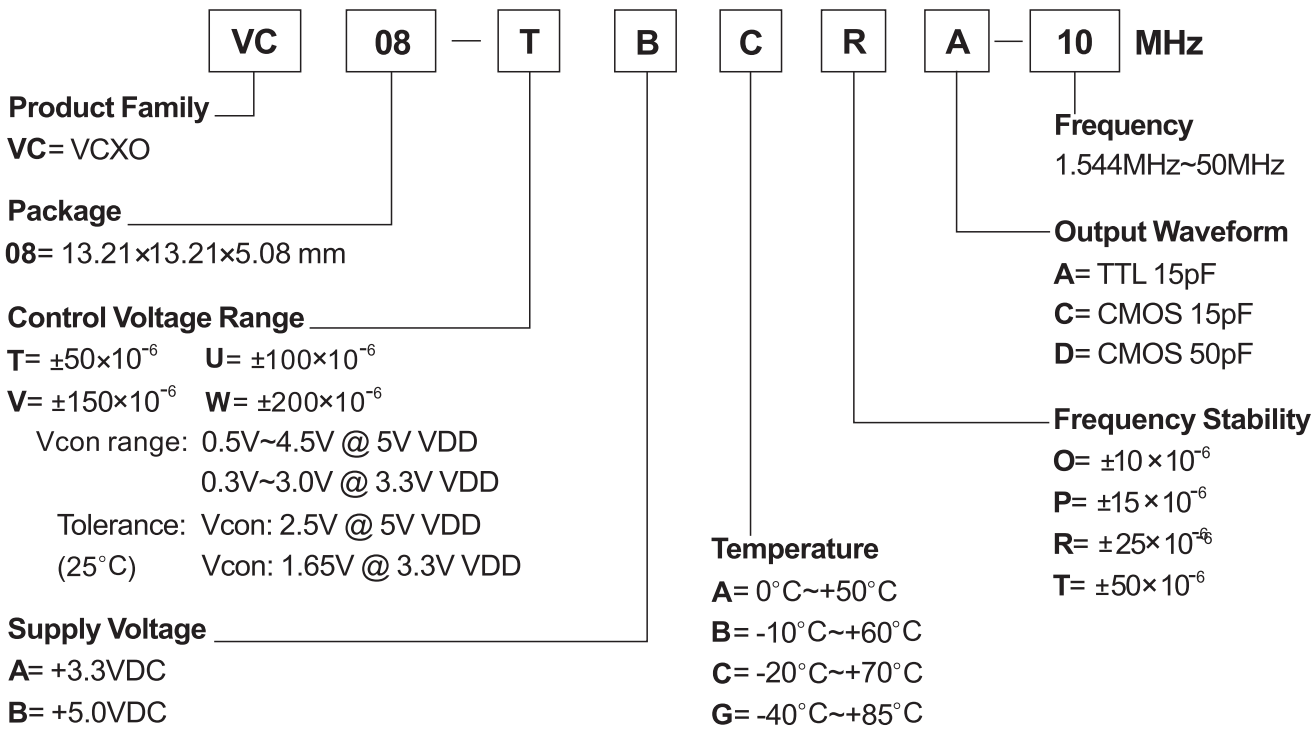
Products Information

VC08

FREQUENCY STABILITY OVER TEMPERATURE RANGE

Temperature Range	Frequency Stability			
	O ± 10 × 10 ⁻⁸	P ± 15 × 10 ⁻⁸	R ± 15 × 10 ⁻⁸	T ± 15 × 10 ⁻⁸
A: 0°C~+50°C	●	●	●	●
B: -10°C~+60°C	●	●	●	●
C: -20°C~+70°C	●	●	●	●
G: -40°C~+85°C			●	●

ORDERING INFORMATION



ORDERING EXAMPLE

VC08-TBCRA-10MHz
VCXO / Pulling Range: ±50×10⁻⁶ / +5.0VDC / -20°C~+70°C / Frequency Stability: ±25×10⁻⁶ / TTL 15pF / 10MHz

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